

**KEY PROGRAMME INFORMATION**

|   |   |
|---|---|
| <b>Originating institution(s)</b><br>Bournemouth and Poole College  | <b>Faculty responsible for the programme</b><br>Faculty of Science and Technology   |
| <b>Final award(s), title(s) and credits</b><br>FdEng Engineering (Electronic Design) – 120 (60 ECTS) Level 5<br><br>Students who undertake this award may do so in order to meet the academic requirements of the Manufacturing Engineer degree apprenticeship route.   |   |
| <b>Intermediate award(s), title(s) and credits</b><br>Not applicable  |   |
| <b>UCAS Programme Code(s) (where applicable and if known)</b><br>H610   | <b>HECoS (Higher Education Classification of Subjects) Code and balanced or major/minor load</b><br>100184<br>100165<br>100374  |
| <b>External reference points</b> <ul style="list-style-type: none"> <li>• UK Quality Code for Higher Education (The QAA):</li> <li>• Part A: Setting and Maintaining Academic Standards</li> <li>• Part B: Assuring and Enhancing Academic Quality</li> <li>• Subject benchmark statements - Engineering (2015)</li> <li>• UK Standard for Professional Engineering Competence: Engineering Technician, Incorporated Engineer and Chartered Engineer Standard (UK-SPEC) third edition from the Engineering Council UK (January 2014)</li> <li>• UK Standard for Professional Engineering Competence: The Accreditation of Higher Education Programmes third edition from the Engineering Council UK (May 2014)</li> </ul> |   |
| <b>Professional, Statutory and Regulatory Body (PSRB) links</b><br>Accredited by Institution of Engineering Designers (IED)   |   |
| <b>Places of delivery</b><br>Bournemouth and Poole College, North Road Campus   |   |
| <b>Mode(s) of delivery</b><br>Part-time/Full-time   | <b>Language of delivery</b><br>English  |
| <b>Typical duration</b><br>Programme duration: 1 calendar year  |   |
| <b>Date of first intake</b><br>July 2019  | <b>Expected start dates</b><br>July   |
| <b>Maximum student numbers</b><br>Not applicable  | <b>Placements</b><br>Part-time: Not applicable<br>Full-time: Compulsory 500 hours placement throughout the calendar year. Students are expected to search for suitable placement opportunities, with the support of the team. |
| <b>Partner(s)</b><br>Bournemouth and Poole College  | <b>Partnership model</b><br>Franchise   |
| <b>Date of this Programme Specification</b><br>June 2019  |   |
| <b>Version number</b><br>Version 1.1-0723   |   |
| <b>Approval, review or modification reference numbers</b><br>E201718 44<br>BU1819 01  |   |
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## Programme Specification – Section 1

### PROGRAMME STRUCTURE

| <b>Programme Award and Title:</b> FdEng Engineering (Electronic Design)<br><b>Admission Requirements:</b> 120 credits at Level 4, with a minimum classification of Merit, from a relevant qualification<br><b>Internal Progression:</b> from HNC Engineering programmes |                 |                  |                                  |          |          |  |                        |                       |
|---|-----------------|------------------|----------------------------------|----------|----------|--|------------------------|-----------------------|
| <b>Level 5</b><br>Students are required to complete all 6 core units  |                 |                  |                                  |          |          |  |                        |                       |
| Unit Name   | Core/<br>Option | No of<br>credits | Assessment Element<br>Weightings |          |          | Expected<br>contact<br>hours per<br>unit | Unit<br>version<br>no. | HECoS Subject<br>Code |
|   |                 |                  | Exam<br>1                        | Cwk<br>1 | Cwk<br>2 |  |                        |                       |
| Applied Software in Engineering   | Core            | 20               |                                  | 60%      | 40%      | 110                                      | 1.0                    | 100374                |
| Electronic Design Applications  | Core            | 20               |                                  | 50%      | 50%      | 110                                      | 3.0                    | 100165                |
| Major Project   | Core            | 40               |                                  | 100%     |          | 110                                      | 3.0                    | 100184                |
| The Engineering Professional  | Core            | 20               |                                  | 50%      | 50%      | 110                                      | 3.0                    | 100184                |
| Work Based Unit   | Core            | 20               |                                  | 100%     |          | 110                                      | 3.0                    | 100184                |
|   |                 |                  |                                  |          |          |  |                        |                       |
|   |                 |                  |                                  |          |          |  |                        |                       |
|   |                 |                  |                                  |          |          |  |                        |                       |
| <b>Progression requirements:</b> Not applicable<br><b>Exit qualification:</b> FdEng Engineering (Electronic Design) (requires 120 credits at Level 4 and 120 credits at Level 5)  |                 |                  |                                  |          |          |  |                        |                       |

### AIMS OF THE DOCUMENT

The aims of this document are to:

- define the structure of the programme;
- specify the programme award titles;
- identify programme and level learning outcomes;
- articulate the regulations governing the awards defined within the document.

### AIMS OF THE PROGRAMME

This programme aims to develop critically informed, agile and resourceful graduates, who:

- have a range of knowledge and skills to enable successful operation within electronic engineering design
- have the transferable skills necessary to operate effectively within electronic engineering design
- have the analytical and research skills necessary for electronic engineering design
- have the ability to reflect upon the shortfalls
- are competent users of computer aided engineering and the management of modern technologies
- are competent practitioners in hardware and software design necessary for electronic engineering
- are competent in simulation techniques necessary for electronic engineering

The overall aim of the programme is to produce highly employable graduates who combine an in-depth knowledge and skills in electronic engineering design with a wide ranging understanding of more general professional requirements. They will be critical thinkers and independent learners, able to solve complex design/engineering-related problems individually and in teams, and to critically evaluate these solutions

These aims have been aligned with the Engineering Design Specific Learning Outcomes for Engineering Council (UK) Accredited Degree Programmes. Graduates from accredited degree programmes must achieve a number of learning outcomes incorporating the key skills of knowledge and understanding, intellectual abilities, practical skills and general transferable skills. The learning outcomes are expressed in terms of science and mathematics; engineering analysis; design; economic, legal, social, ethical and environmental context; engineering practice; and additional general skills.

A key feature of this programme is its industrial relevance and close ties with the requirements of local industry. As the majority of the students are employed within engineering, projects can be 'live' and assignments related to and based on real experiences within industry. Seminars and group work give the students the opportunity to elaborate on their experiences within industry to their peers and therefore expand knowledge beyond a purely academic sense.

### ALIGNMENT WITH THE UNIVERSITY'S STRATEGIC PLAN

The FdEng Engineering (Electronic Design) is informed by and aligned with Bournemouth University's 2012-18 strategic plan and the fusion agenda. The programme will substantially enhance, through external employer/industry engagement, the core area engineering education, thus creating fusion in action. It will continue to significantly enhance engagement with this industry sector (Creating/Inspiring/Sharing), with the opportunity to further expand student numbers. Well-formed industrial relationships have developed real 'live' projects that benefit local industry, the students and academia. Further opportunities such as industrial visits and work based briefs also result in enhancing the student experience for the whole of the department.

### LEARNING HOURS AND ASSESSMENT

FdEng Engineering (Electronic Design)

Version 1.1-0723

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## Programme Specification - Section 2

Bournemouth University taught programmes are composed of units of study, which are assigned a credit value indicating the amount of learning undertaken. The minimum credit value of a unit is normally 20 credits, above which credit values normally increase at 20-point intervals. 20 credits is the equivalent of 200 study hours required of the student, including lectures, seminars, assessment and independent study. 20 University credits are equivalent to 10 European Credit Transfer System (ECTS) credits.

The assessment workload for a unit should consider the total time devoted to study, including the assessment workload (i.e. formative and summative assessment) and the taught elements and independent study workload (i.e. lectures, seminars, preparatory work, practical activities, reading, critical reflection).

Assessment per 20 credit unit should normally consist of 3,000 words or equivalent. Dissertations and Level 6 and 7 Final Projects are distinct from other assessment types. The word count for these assignments is 5,000 words per 20 credits, recognising that undertaking an in-depth piece of original research as the capstone to a degree is pedagogically sound.

### STAFF DELIVERING THE PROGRAMME

Students will usually be taught by a combination of senior academic staff with others who have relevant expertise including – where appropriate according to the content of the unit – academic staff, qualified professional practitioners, demonstrators/technicians and research students.

### INTENDED LEARNING OUTCOMES – AND HOW THE PROGRAMME ENABLES STUDENTS TO ACHIEVE AND DEMONSTRATE THE INTENDED LEARNING OUTCOMES

#### LEVEL 5/FdEng INTENDED LEVEL OUTCOMES

|  |   |
|--|---|
| <p><b>A: Knowledge and understanding</b></p> <p>This programme provides opportunities for students to develop and demonstrate knowledge and understanding of:</p>  | <p>The following learning and teaching and assessment strategies and methods enable students to achieve and to demonstrate the level learning outcomes:</p>   |
| <p><b>A1</b> Routine and non-routine design problems and appropriate mathematical skills</p> <p><b>A2</b> Wide range of scientific theories appropriate to design</p> <p><b>A3</b> ITC relevant to advanced engineering design</p> <p><b>A4</b> Professional and ethical responsibilities</p> <p><b>A5</b> Regulatory framework for safe engineering practice</p> <p><b>A6</b> Design techniques for the solution of routine and non-routine problems in the engineering context</p> | <p>Learning and teaching strategies and methods (referring to numbered Intended Learning Outcomes):</p> <ul style="list-style-type: none"> <li>• lectures (A1-A2, A4-A6);</li> <li>• seminars (A1-A6);</li> <li>• directed reading (A3-A6);</li> <li>• use of the VLE (A1-A2, A4, A6);</li> <li>• project (A1-A6).</li> </ul> <p>Assessment strategies and methods (referring to numbered Intended Learning Outcomes):</p> <ul style="list-style-type: none"> <li>• individual reports (A1-4, A6);</li> <li>• group reports (A1-A6);</li> </ul> |

## Programme Specification - Section 2

|  |  |
|--|--|
|  | <ul style="list-style-type: none"> <li>• professional review (A1-A6);</li> <li>• unseen in-class tests (A1-A2).</li> </ul>   |
| <p><b>B: Intellectual skills</b></p> <p>This programme provides opportunities for students to:</p>   | <p>The following learning and teaching and assessment strategies and methods enable students to achieve and to demonstrate the level learning outcomes:</p>  |
| <p><b>B1</b> Use mathematical and scientific techniques in the analysis of routine and non-routine engineering design problems with minimal guidance</p> <p><b>B2</b> Use computer based techniques in the analysis of routine and non-routine engineering design problems with minimal guidance</p> <p><b>B3</b> Analyse routine and non-routine engineering design problems at system, process and component level with minimal guidance</p> <p><b>B4</b> Analyse routine and non-routine engineering design problems relating to balancing of cost, benefit and aesthetics with minimal guidance</p> <p><b>B5</b> Develop new processes or products through the synthesis of ideas and data gathered from a wide range of sources</p> | <p>Learning and teaching strategies and methods (referring to numbered Intended Learning Outcomes):</p> <ul style="list-style-type: none"> <li>• lectures (B1-B5);</li> <li>• seminars (B1-B5);</li> <li>• directed reading (B4-B5);</li> <li>• use of the VLE (B1-B3);</li> <li>• project (B1-B5).</li> </ul> <p>Assessment strategies and methods (referring to numbered Intended Learning Outcomes):</p> <ul style="list-style-type: none"> <li>• individual reports (B1-B5);</li> <li>• group reports (B1-B5);</li> <li>• professional review (B1-B4);</li> <li>• unseen in-class tests (B1, B3).</li> </ul> |
| <p><b>C: Practical skills</b></p> <p>This programme provides opportunities for students to:</p>  | <p>The following learning and teaching and assessment strategies and methods enable students to achieve and to demonstrate the level learning outcomes:</p>  |
| <p><b>C1</b> Use appropriate test and measurement equipment for experimental laboratory investigation with minimal guidance</p> <p><b>C2</b> Use engineering CAD, CAM and RM software to aid engineering design with minimal guidance</p> <p><b>C3</b> Analyse experimental methods to evaluate the performance of engineering products or systems with minimal guidance</p> <p><b>C4</b> Plan, schedule and execute routine and non-routine projects within an engineering context with minimal guidance</p>  | <p>Learning and teaching strategies and methods (referring to numbered Intended Learning Outcomes):</p> <ul style="list-style-type: none"> <li>• lectures (C1-C4);</li> <li>• seminars (C1-C4);</li> <li>• use of the VLE (C4);</li> <li>• project (C1-C4).</li> </ul> <p>Assessment strategies and methods (referring to numbered Intended Learning Outcomes):</p>  |

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|  |  |
|--|--|
|  | <ul style="list-style-type: none"> <li>• individual reports (C1-C4);</li> <li>• group reports (C2, C4);</li> <li>• professional review (C1-C4).</li> </ul>   |
| <p><b>D: Transferable skills</b></p> <p>This programme provides opportunities for students to:</p>   | <p>The following learning and teaching and assessment strategies and methods enable students to achieve and to demonstrate the level learning outcomes:</p>  |
| <p><b>D1</b> Operate effectively in commerce or industry in a wide range of different situations with minimal guidance</p> <p><b>D2</b> Analyse the outcomes of actions taken and reflect upon their effects with minimal guidance</p> <p><b>D3</b> Communicate effectively through report writing, presentation and debate</p> <p><b>D4</b> Take leadership roles within teams and/or projects in both education and in the workplace</p> | <p>Learning and teaching strategies and methods (referring to numbered Intended Learning Outcomes):</p> <ul style="list-style-type: none"> <li>• lectures (D1-D3);</li> <li>• seminars (D1-D4);</li> <li>• use of the VLE (D2-D3);</li> <li>• project (D1-D4).</li> </ul> <p>Assessment strategies and methods (referring to numbered Intended Learning Outcomes):</p> <ul style="list-style-type: none"> <li>• individual reports (D1-D4);</li> <li>• group reports (D2-D4);</li> <li>• professional review (D1-D4).</li> </ul> |

### ADMISSION REGULATIONS

The regulations for this programme are the University's Standard Undergraduate Admission Regulations with the following exceptions:

- 120 Level 4 credits from an HNC in an appropriate discipline are required.
- The HNC should be awarded with a minimum classification of Merit.

### PROGRESSION ROUTES

#### Internal Progression

Internal progression onto the FdEng Engineering (Electronic Design) programme, with advanced standing, is available to students who have successfully completed one of the HNC Engineering programmes with a minimum classification of Merit.

Students who have successfully completed the FdEng Engineering (Electronic Design) programme at Bournemouth and Poole College with a minimum classification of Merit will be automatically accepted for entry with advanced standing to Level 6 of the BEng (Hons) Engineering or MEng (Hons) Engineering programmes at Bournemouth University and credited with 120 credits at Level 4 and 120 credits at Level 5.

Or

Students who have successfully completed the FdEng Engineering (Electronic Design) programme with a minimum classification of Merit will be eligible to apply for entry with advanced standing to the Level 6 of the MEng / BEng (Hons) Mechanical Engineering (Full-time) programme at Bournemouth University and credited with 120 credits at Level 4 and 120 credits at Level 5.

### ASSESSMENT REGULATIONS

The regulations for this programme are the University's Standard Foundation Degree Assessment Regulations (<https://intranetsp.bournemouth.ac.uk/pandptest/6a-standard-assessment-regulations-foundation.pdf>) with the following exceptions:

#### COMPENSATION (Section 7)

Compensation may only be applied for up to 20 credits at level 5.

### WORK BASED LEARNING (WBL) AND PLACEMENT ELEMENTS

An assessed Work Based learning unit is incorporated within the FdEng Programme and offers an opportunity for learners to obtain credit for and to reflect upon their learning either formally by way of in-service training courses, or informally on a day-to-day basis in the workplace. Reflective logs are used to facilitate students' learning and encourage the transferability of knowledge between study and the workplace. The unit is designed to encourage the students to review what they do at work in light of the UK Standard for Professional Engineering Competence (UK-SPEC), fulfilling the competence and commitment requirements for registration as an Incorporated Engineer (IEng).

As the part time students are normally employed in the engineering industry, all units offer informal opportunity for reflection on current practice which may be documented subsequently as part of the work based unit.

All students, both full-time and part-time, undertake an industry related Major Project. This can be carried out within a company or developed within the college environment. In both cases the projects involve direct contact with the customer. When a project is carried out at BPC, students will normally design and develop a project specified by a company.

## Programme Specification - Section 2

It is also a requirement for the full-time students, or part-time students not in relevant employment, to undertake a work placement within local industry during the programme's calendar year, of the minimum of 30 weeks or 1200 hours. This is to further their understanding of industrial practice and appreciation of a real engineering working environment. This period will enable the students to complete a reflective logbook for use in the Work Based learning unit.

Site visits, presentations by and discussions with industry representatives will also ensure that a "real world" understanding of project management is achieved.

For learners undertaking a Higher or Degree Apprenticeship supporting evidence for NVQ 4 or End-Point Assessment requirements can be taken from the Work Based Unit report and the work based learning elements of the programme.



**Programme Specification - Section 2**

**Programme Skills Matrix**

| Units  |                                 | Programme Intended Learning Outcomes |     |     |     |     |     |     |     |     |     |  |     |     |     |     |     |     |     |     |     |     |     |   |
|--|---------------------------------|--------------------------------------|-----|-----|-----|-----|-----|-----|-----|-----|-----|--|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|---|
|  |                                 | A 1                                  | A 2 | A 3 | A 4 | A 5 | A 6 | B 1 | B 2 | B 3 | B 4 | B 5  | B 6 | C 1 | C 2 | C 3 | C 4 | C 5 | C 6 | D 1 | D 2 | D 3 | D 4 |   |
| LEVEL 5  | Applied Software in Engineering | *                                    | *   | *   |     | *   | *   | *   | *   | *   |     | *  |     | *   | *   | *   |     |     |     |     |     |     |     |   |
|  | Electronic Design Applications  | *                                    | *   | *   |     | *   | *   | *   | *   | *   |     | *  |     | *   | *   | *   |     |     |     |     |     |     |     |   |
|  | Major Project                   | *                                    | *   | *   | *   | *   | *   | *   | *   | *   | *   |  |     | *   |     |     | *   |     |     |     | *   | *   |     |   |
|  | The Engineering Professional    |                                      |     |     | *   | *   |     |     |     |     |     | *  |     |     |     |     |     |     |     |     | *   | *   | *   | * |
|  | Work Based Unit                 | *                                    | *   | *   | *   | *   | *   | *   | *   | *   | *   | *  |     |     | *   |     |     | *   |     |     | *   | *   | *   | * |
|  |                                 |                                      |     |     |     |     |     |     |     |     |     |  |     |     |     |     |     |     |     |     |     |     |     |   |
| <p><b>A – Subject Knowledge and Understanding</b><br/>                     This programme provides opportunities for students to develop and demonstrate knowledge and understanding of:</p> <ol style="list-style-type: none"> <li>1. Routine and non-routine design problems and appropriate mathematical skills</li> <li>2. Wide range of scientific theories appropriate to design</li> <li>3. ITC relevant to advanced engineering design</li> <li>4. Professional and ethical responsibilities</li> <li>5. Regulatory framework for safe engineering practice</li> <li>6. Design techniques for the solution of routine and non-routine problems in the engineering context</li> </ol>   |                                 |                                      |     |     |     |     |     |     |     |     |     | <p><b>C – Subject-specific/Practical Skills</b><br/>                     This programme provides opportunities for students to:</p> <ol style="list-style-type: none"> <li>1. Use appropriate test and measurement equipment for experimental laboratory investigation with minimal guidance</li> <li>2. Use engineering CAD, CAM and RM software to aid engineering design with minimal guidance</li> <li>3. Analyse experimental methods to evaluate the performance of engineering products or systems with minimal guidance</li> <li>4. Plan, schedule and execute routine and non-routine projects within an engineering context with minimal guidance</li> </ol> |     |     |     |     |     |     |     |     |     |     |     |   |
| <p><b>B – Intellectual Skills</b><br/>                     This programme provides opportunities for students to:</p> <ol style="list-style-type: none"> <li>1. Use mathematical and scientific techniques in the analysis of routine and non-routine engineering design problems with minimal guidance</li> <li>2. Use computer based techniques in the analysis of routine and non-routine engineering design problems with minimal guidance</li> <li>3. Analyse routine and non-routine engineering design problems at system, process and component level with minimal guidance</li> <li>4. Analyse routine and non-routine engineering design problems relating to balancing of cost, benefit and aesthetics with minimal guidance</li> <li>5. Develop new processes or products through the synthesis of ideas and data gathered from a wide range of sources</li> </ol> |                                 |                                      |     |     |     |     |     |     |     |     |     | <p><b>D – Transferable Skills</b><br/>                     This programme provides opportunities for students to:</p> <ol style="list-style-type: none"> <li>1. Operate effectively in commerce or industry in a wide range of different situations with minimal guidance</li> <li>2. Analyse the outcomes of actions taken and reflect upon their effects with minimal guidance</li> <li>3. Communicate effectively through report writing, presentation and debate</li> <li>4. Take leadership roles within teams and/or projects in both education and in the workplace</li> </ol>  |     |     |     |     |     |     |     |     |     |     |     |   |

Programme Specification - Section 2

THE SUMMARY OF UK-SPEC SPECIFIC LEARNING OUTCOMES

|                              | Units  | Level | O or C? | US1i     | US2i     | US3      | E1i      | E2i      | E3i      | E4i      | D1i      | D2i      | D3       | D4i      | D5i      | D6i      | S1       | S2       | S3       | S4       | S5       | P1i      | P2i      | P3i      | P4i      | P5       | P6i      | P7i      | P8i      |   |
|------------------------------|--|-------|---------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|---|
| HNC                          | Business and Project Management                | 4     | C       |          |          |          | ✓        | ✓        |          |          |          |          |          |          |          |          | ✓        | ✓        | ✓        | ✓        |          |          |          |          |          |          |          |          |          |   |
|                              | Computer and Internet Technologies             | 4     | C       |          |          |          | ✓        |          |          | ✓        |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |   |
|                              | Project  | 4     | C       | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        |          | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        |          | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓ |
|                              | Circuit Analysis and Fault Location Techniques | 4     | C       |          |          |          | ✓        | ✓        | ✓        |          |          |          |          |          |          | ✓        |          |          |          |          |          |          |          |          | ✓        |          |          |          |          |   |
|                              | Electronic Systems Design                      | 4     | C       |          |          |          |          | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        |          |          |          |          |          |          | ✓        |          | ✓        | ✓        |          |          | ✓        |   |
|                              | Analytical Methods for Design                  | 4     | C       | ✓        | ✓        |          | ✓        | ✓        |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |   |
| FdEng                        | The Engineering Professional                   | 5     | C       |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          | ✓        | ✓        | ✓        |          |          |          |          |          |          |          |   |
|                              | Work Based Unit                                | 5     | C       | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓ |
|                              | Major Project                                  | 5     | C       | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓ |
|                              | Applied Software in Engineering                | 5     | C       |          |          |          | ✓        |          | ✓        | ✓        |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |          |   |
|                              | Electronic Design Applications                 | 5     | C       |          |          |          | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        | ✓        |          |          |          |          |          |          | ✓        |          | ✓        | ✓        |          |          | ✓        |   |
| <b>Total HNC</b>             |  |       |         | <b>2</b> | <b>2</b> | <b>1</b> | <b>5</b> | <b>5</b> | <b>3</b> | <b>3</b> | <b>2</b> | <b>2</b> | <b>1</b> | <b>2</b> | <b>1</b> | <b>3</b> | <b>2</b> | <b>2</b> | <b>2</b> | <b>2</b> | <b>0</b> | <b>2</b> | <b>1</b> | <b>2</b> | <b>3</b> | <b>1</b> | <b>1</b> | <b>2</b> | <b>1</b> |   |
| <b>Total FdEng</b>           |  |       |         | <b>2</b> | <b>2</b> | <b>2</b> | <b>4</b> | <b>3</b> | <b>4</b> | <b>4</b> | <b>3</b> | <b>3</b> | <b>3</b> | <b>3</b> | <b>2</b> | <b>3</b> | <b>2</b> | <b>2</b> | <b>3</b> | <b>3</b> | <b>3</b> | <b>3</b> | <b>2</b> | <b>3</b> | <b>3</b> | <b>2</b> | <b>2</b> | <b>3</b> | <b>2</b> |   |
| <b>Total HNC &amp; FdEng</b> |  |       |         | <b>4</b> | <b>4</b> | <b>3</b> | <b>9</b> | <b>8</b> | <b>7</b> | <b>7</b> | <b>5</b> | <b>5</b> | <b>4</b> | <b>5</b> | <b>3</b> | <b>6</b> | <b>4</b> | <b>4</b> | <b>5</b> | <b>5</b> | <b>3</b> | <b>5</b> | <b>3</b> | <b>5</b> | <b>6</b> | <b>3</b> | <b>3</b> | <b>5</b> | <b>3</b> |   |